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March 26, 2008

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: Title: TRANSISTOR GATE AND LOCAL INTERCONNECT  
Letters Patent No. 7,176,096  
Application No. 10/631,921  
Issued: February 13, 2007  
Our Reference: MICRON.104DV1

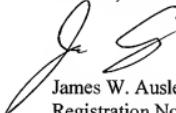
Dear Sir:

Enclosed for filing is a Certificate of Correction in connection with the above-identified patent.

As the errors cited in the Certificate of Correction were incurred through the fault of the Applicant, payment of \$100 is being submitted through the EFS Web System. Please charge any additional fees to our Deposit Account No. 11-1410.

Respectfully submitted,

Knobbe, Martens, Olson & Bear, LLP



James W. Ausley  
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Enclosures

4928842/hlc  
022508

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

**PATENT NO.** : 7,176,096  
**APPLICATION NO.** : 10/631,921  
**ISSUE DATE** : February 13, 2007  
**INVENTOR(S)** : Jigish D. Trivedi

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1

Line 34, delete "suicides" and insert - - silicides - -.

Column 6

Line 62 – Delete "H<sub>2</sub>O:H<sub>2</sub>O<sub>2</sub>:NH<sub>4</sub>OH" and insert - - H<sub>2</sub>O:H<sub>2</sub>O<sub>2</sub>:NH<sub>4</sub>OH - -.

Column 7

Line 58, Claim 1, delete "directly-contacts" and insert - - directly contacts - -.

Column 10

Line 34, Claim 22, after "comprises" insert - - : - -.

Line 37, Claim 22, delete "suicides" and insert - - silicides - -.

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